

**AMENDMENTS TO THE SPECIFICATION:**

Substitute the following paragraph for Page 5, first paragraph.

" Figure 1 shows the relationship of a central processor 102 to an event recording mechanism 104 that captures various events 110, 112, 114, 116 from various sources within a computer system. While the current invention relates specifically to events received from an ~~Input~~ Input/Output interface, this invention could just as well be used in any apparatus used to record events from a wide variety of sources. For example, the recording mechanism could be applied to a system used to monitor many different kinds of events such as system errors, temperature alerts, etc. The processor 102 comprises a main memory 108. Within the recording mechanism is a circular FIFO 120 and control logic. The connection between the processor 102 and the recording mechanism 104 is a computer bus 106 comprising several functions:

- 1) The ability for the processor to load information from the recording mechanism into one of the processor's general registers,
- 2) The ability of the processor to store information into the recording mechanism from one of the processor's general registers,
- 3) The ability of the recording mechanism to store blocks of data into the processor's main memory, and
- 4) The ability of the recording mechanism to send interruption signals to the processor."